



2133 \$  
PATENT  
#2  
2-5-03  
AM

Applicant(s): Philip D. Steiner et al.

Title: METHOD AND APPARATUS FOR DETERMINING A LOSS OF SIGNAL CONDITION

Application No.: 10/015,030

Filed: December 11, 2001

Examiner: Unknown

Group Art Unit: 2133

Atty. Docket No.: 026-0013

RECEIVED  
MAY 02 2002  
Technology Center 2600

April 4, 2002

COMMISSIONER FOR PATENTS  
Washington, DC 20231

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. § 1.97**

RECEIVED  
APR 17 2002  
Technology Center 2100

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO-1449  
☐ Other: n/a

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

1. an admission that the information necessarily is, or corresponds to, prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described below; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance through (i) an English language abstract, (ii) an English language equivalent application, or (iii) if cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that indicates the degree of relevance found by the foreign office.

04/17/2002 RHEBRAHT 00000091 500631 10015030

01 FC:126 180.00 CH

Adjustment date: 04/16/2003 LMARKS  
04/17/2002 RHEBRAHT 00000091 500631 10015030  
01 FC:126 180.00 CR

FEE AUTHORIZATION

- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
- ☒ The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.
- ☒ If however, this Information Disclosure Statement is filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 50-0631.

CERTIFICATE OF MAILING

I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231.

  
Mark Zagorin

4/4/02  
Date

Respectfully submitted,



Mark Zagorin, Reg. No. 36,067  
Attorney for Applicant(s)  
(512) 347-9030  
(512) 347-9031 (fax)

U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 026-0013		
				Application No.: 10/015,030		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Applicant(s): Philip D. Steiner et al.		
(Use several sheets if necessary)				Filing Date: December 11, 2001		
				Group Art Unit: 2133		
				Date Submitted: April 4, 2002		
<b>U.S. Patent Documents</b>						
<b>*Examiner Initial</b>		<b>Document Number</b>	<b>Date</b>	<b>Name</b>		
	AA			<b>RECEIVED</b> MAY 02 2002 Technology Center 2600		
	AB					
	AC					
	AD					
	AE					
	AF			<b>RECEIVED</b> APR 17 2002 Technology Center 2100		
	AG					
	AH					
	AI					
	AJ					
	AK					
<b>Foreign Patent Documents</b>						
				<b>Translation</b>		
		<b>Document</b>	<b>Date</b>	<b>Country</b>	<b>Yes</b>	<b>No</b>
	AL					
	AM					
	AN					
	AP					
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>						
	AR	Giga, "2.5 Gbit/s Clock and Data Recovery GD16522", Data Sheet Rev. 20, Giga, September 25, 2000, pp. 1-11				
	AS	Maxim, "2.5Gbps, Low-Power, +3.3V Clock Recovery and Data Retiming IC", Maxim Integrated Products, 1998, pp. 1-8				
	AT					
<b>Examiner</b>			<b>Date Considered</b>			
<p><b>*EXAMINER:</b> Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>						